04230.P3026C <u>Patent</u> IN THE UNITED STATES PATENT AND TRADEMARK OFFICE In re application of: Vikas Aditya Prakash Iyer Examiner: Eng, D Serial No. Art Group 2315 Filed: SUPER PIPELINED ARCHITECTURE For: FOR TRANSMIT FLOW IN A NETWORK CONTROLLER A Rule 53(d) Continuation of U.S. Patent Application Serial No. 08/526,714 Filed: September 11, 1995 ASSISTANT COMMISSIONER FOR PATENTS Washington, D.C. 20231

PRELIMINARY AMENDMENT AND REMARKS

Dear Sir:

In connection with the accompanying Rule 53(d) continuation application for the above captioned parent application, please enter the following amendment and consider the associated remarks.

IN THE CLAIMS

Please amend claim 22 as follows:

22. (Once Amended) A process comprising:

copying data to be transmitted over a network from a main memory in a host computer to a buffer memory in a network controller for so long as unused memory locations remain in said buffer memory;

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